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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

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|---|---------------------------|--------------|-------------------|--------------------------------------|----------------------------|
| APPL NUM 10044501 | FILING DATE 01/10/2002 | CLASS 324 | SUBCLASS 158.1 | GAU 2829 | EXAMINER J. W. J. J. J. |
| **APPLICANTS: Babcock Douglas; | | | | | |
| **CONTINUING DATA VERIFIED: THIS APPLICATION IS A CIP OF 09/757,746 01/10/2001 AND CLAIMS BENEFIT OF 60/265,729 02/01/2001 AND CLAIMS BENEFIT OF 60/267,162 02/07/2001 | | | | | |
| ** FOREIGN APPLICATIONS VERIFIED: | | | | | |
| PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/> | | | | | |
| Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged Examiners's initials | | | | ATTORNEY DOCKET NO Analog 5167CIP | |
| TITLE : Apparatus and method for driving circuit pins in a circuit testing system <small>U.S. DEPT. OF COMM./PAT & TM-PTO-436L (Rev. 12-94)</small> | | | | | |

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|---|-----------|-----------------------------|------------------------|
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | Print Claim for O.G. |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Sheets Drwg. | Figs. Drwg. Print Fig. |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | Application Examiner | |
| | | PREPARED FOR ISSUE | |
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